

# V.K.R., V.N.B & A.G.K COLLEGE OF ENGINEERING

(Sponsored by General & Technical Education Society, GUDIVADA)

An ISO 9001 : 2015 Certified Institute

(Approved by AICTE - New Delhi & Affiliated to JNTUK, Kakinada)

Eluru Road, **GUDIVADA - 521 301**, Krishna Dist., A.P., INDIA.

Date: 30-01-2020

To

The Registrar  
JNTUK Kakinada  
Andhra Pradesh

Dear Sir

Subject: Incorporating the changes in the curriculum based on the analysis of the feedback collected from Stakeholders for the A.Y 2020-21 - Reg.


Our Institution would like to intimate the University about incorporating the changes in the curriculum. The Institution has collected feedbacks on the curriculum from its Stakeholders after the completion of the semesters.

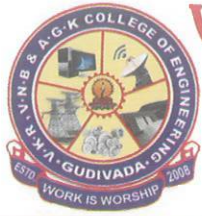
We are here by attaching the copy of feedback analysis and Action Taken Report (ATR) for your perusal.

Thanking you

Regards

  
REGISTRAR  
J.N.T. University Kakinada  
KAKINADA - 533 003

  
30/1/2020  
PRINCIPAL  
PRINCIPAL  
VKR VNB & AGK COLLEGE OF ENGINEERING  
Eluru Road, GUDIVADA, Kri Dt. A.P. 521 301



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Eluru Road, **GUDIVADA - 521 301**, Krishna Dist., A.P., INDIA.

Date: 15-02-2020

From:

HOD

Department of ECE,  
V.K.R., V.N.B. & A.G.K. College of Engineering,  
Gudivada.

To

The Chairman BOS-ECE,  
JNTU, Kakinada.

Through Proper Channel/-

Sub: Identified gaps in some courses in regulation R16, R19: Reg.

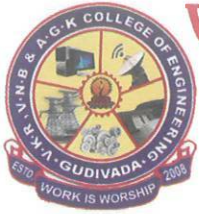
Respected Sir/Madam,

We are from V.K.R., V.N.B. & A.G.K. College of Engineering, Gudivada. Electronics and communication department. Some of these topics are important for gate exam and also for the industry. In this regard, we request you to kindly check and include the topics for the benefit of the students to the respective courses. Kindly consider.

Regards,

HOD

Department of ECE,  
V.K.R., V.N.B. & A.G.K. College of Engineering,  
Gudivada.



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Regulation	Course Name	Topic to be included	Remarks
R16, R19	Electronic Devices and Circuits	Low level injection condition in semiconductor. Simple diode and transistor circuits for problem solving	GATE Helpful for competitive exams like Gate, ISRO, etc.
R16, R19	Electronics Circuit Analysis	Crystal Oscillator	Useful for GATE
R16	Digital Image Processing	Segmentation Techniques	Useful for Projects
R16	Digital IC Applications	Concept of Propagation Delays in Digital Circuits	Useful for GATE
R19	Switching Theory and Logic Design	Digital Logic Families	Useful for GATE
R16	Radar Systems	Radomes	Helpful for competitive exams
R16	Linear IC Applications	Multi-vibrators using Transistors	Helpful for competitive exams

Received copy -

A.m. Prasad

**PROFESSOR OF ECE  
DEPARTMENT OF ECE  
UCEK JNTUK KAKINADA**